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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lin, *et al.* Docket No.: TSM03-0670  
Serial No.: 10/729,092 Art Unit: 2815  
Filed: December 5, 2003 Examiner: Matthew E. Warren  
For: Structure and Method of Forming Integrated Circuits Utilizing Strained Channel Transistors

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Date of Deposit: October 8, 2004

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Information Disclosure Statement (1 page)  
IDS Form PTO/SB/08a and 08b (2 pages) citing (15) references  
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Respectfully submitted,

*Natalie Swider*

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Mail Stop: Amendment  
Commissioner for Patents  
P. O. Box 1450  
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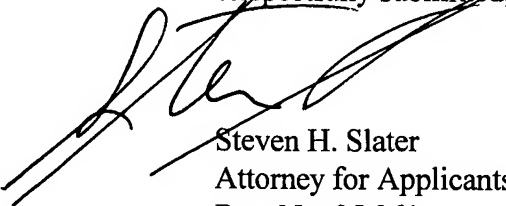
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October 8, 2004

Date

Respectfully submitted,

  
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				Application Number	10/729,092	
				Filing Date	December 5, 2003	
				First Named Inventor	Lin, <i>et al.</i>	
				Art Unit	2815	
				Examiner Name	TBD	
Sheet	2	of	2	Attorney Docket Number		TSM03-0670

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	13	LEITZ, C.W., <i>et al.</i> , "Channel Engineering of SiGe-Based Heterostructures for High Mobility MOSFETs," Materials Research Society Symposium Proceedings, vol. 686, (2002), pp. 113-118.	
	14	LEITZ, C.W., <i>et al.</i> , "Hole mobility enhancements in strained Si/Si <sub>1-y</sub> Ge <sub>y</sub> p-type metal-oxide-semiconductor field-effect transistors grown on relaxed Si <sub>1-x</sub> Ge <sub>x</sub> (x<y) virtual substrates," Applied Physics Letters, Vol. 79, No. 25, pp. 4246-4248, December 17, 2001.	
	15	LIU, K.C., <i>et al.</i> , "A Novel Sidewall Strained-Si Channel nMOSFET," IEDM, (1999) pp. 63-66.	

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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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